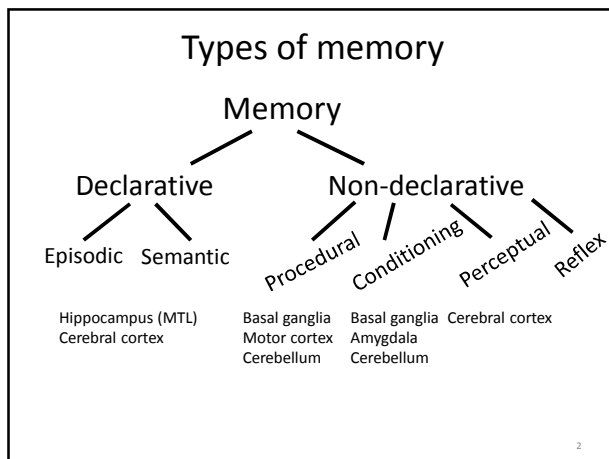


# Systems Neuroscience CISC 3250

## Memory




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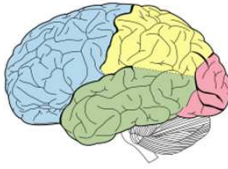
### Declarative vs. non-declarative memory

- Declarative
  - “Spring break ended on March 22”
  - “Apples are edible, chairs are not edible”
- Non-declarative
  - Throwing a baseball
  - Pattern completion (seeing the dog behind the fence)



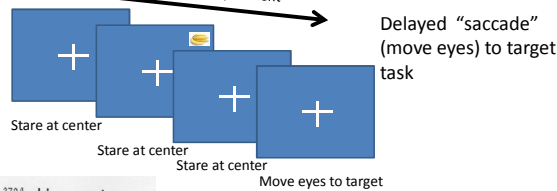
### Short-term vs. long-term memory

- Short-term memory – aka “working” memory
  - Hold facts in memory for 1-200 seconds
  - Sometimes prolonged version of perception
  - Associated with prefrontal cortex (PFC)
- Long-term memory
  - Stores facts over years
  - Associated with hippocampus (also, amygdala)



### Working memory

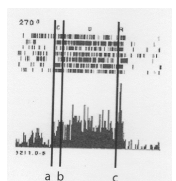
time over experiment



Delayed “saccade” (move eyes) to target task

Neural memory in dlPFC for delayed-action task

a: stimulus display onset  
b: stimulus display offset  
c: performance of action

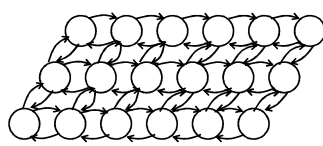


*Funahashi et al. 1989*

Banana picture from Fir0002/Flagstaffotos

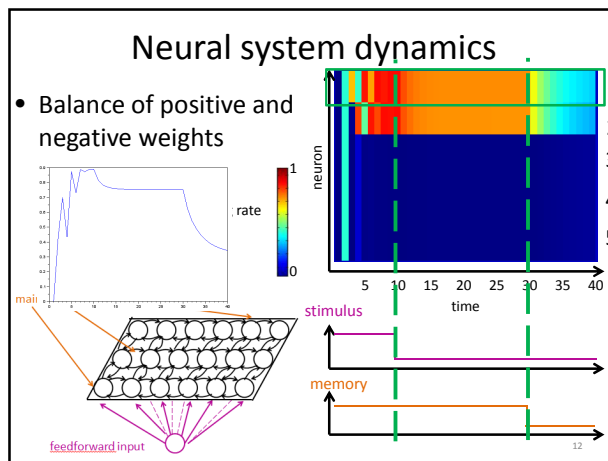
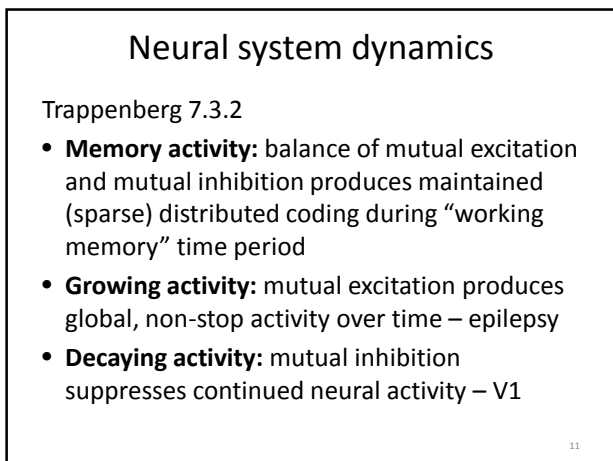
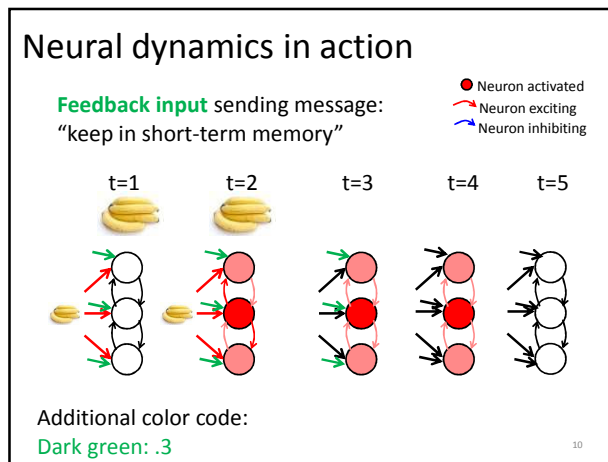
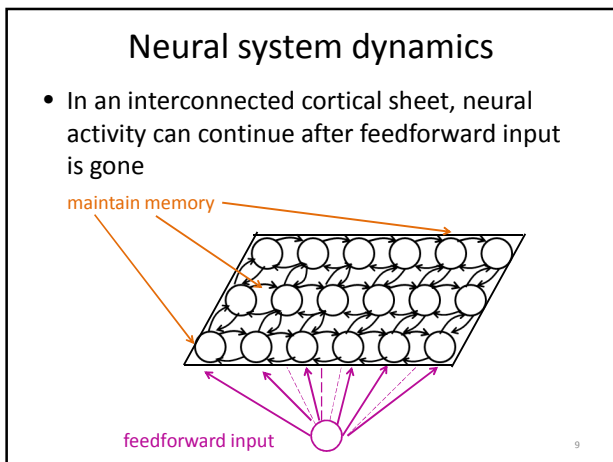
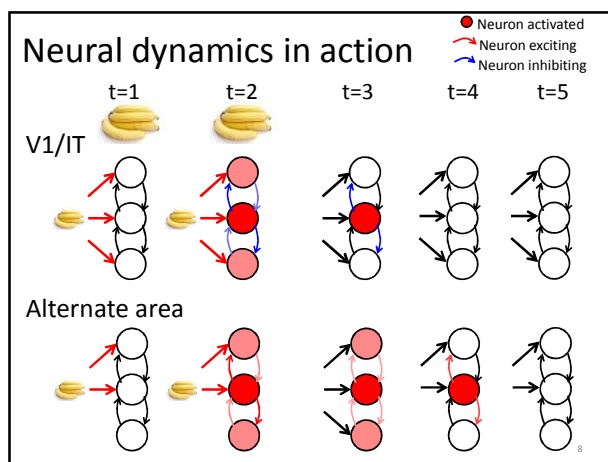
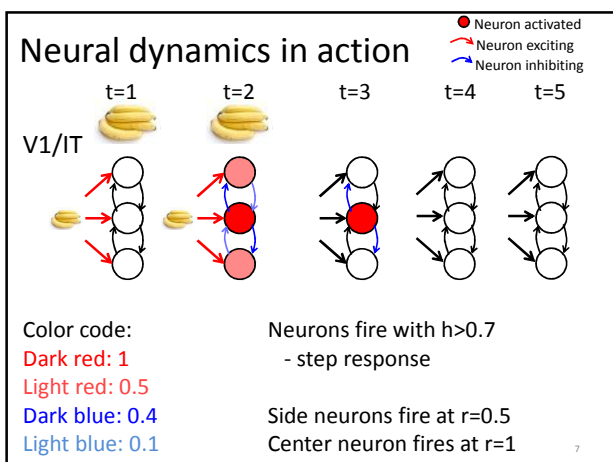
### Neural dynamics in “cortical sheet”

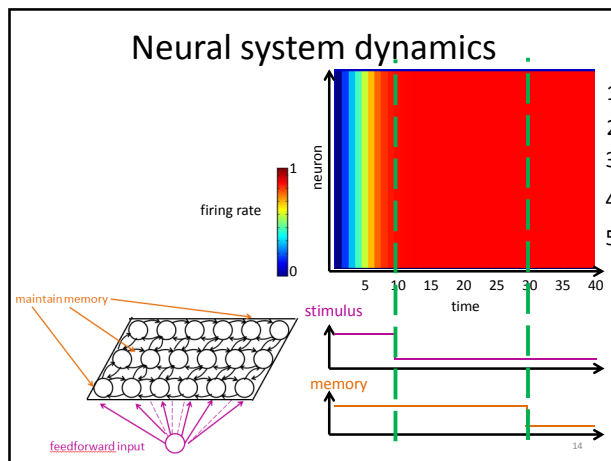
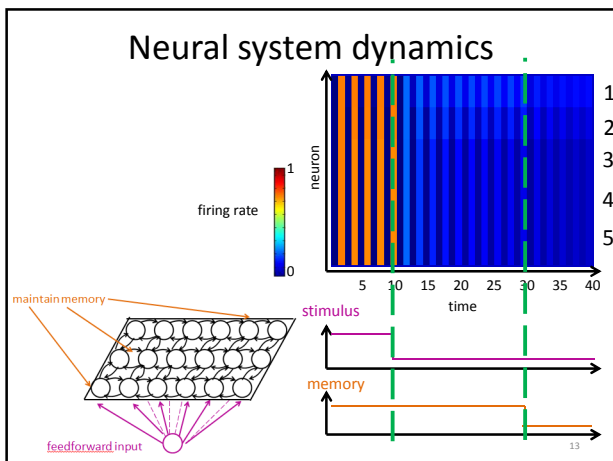
- Cortical sheet: group of neurons on same level of hierarchy interacting with lateral connections
- Balance between local cooperation and local inhibition



- $r^{out}$  determined from

$$h = \left( \sum_j w_j r_j^{feedfwd} \right) + \left( \sum_k w_k r_k^{lateral} \right) + \left( \sum_m w_m r_m^{feedback} \right)$$





### Anatomy of long term memory

Hippocampus ("sea horse")  
 In medial temporal lobe (MTL)

- Input: Entorhinal cortex – EC
- Dentate gyrus – DG
- Cornus ammonis – CA1, CA3
- Perforant pathway: EC -> CA3

Figure 15: Anatomy of long term memory. Includes a brain diagram highlighting the hippocampus, an MRI scan, and a schematic of the hippocampal circuitry showing EC, DG, CA1, CA3, and SB.

### Recurrent networks

- Extensive collateral connections in CA3 enhance associative memory

Figure 16: Recurrent networks. A diagram shows a grid of neurons with extensive collateral connections. A schematic of the hippocampal circuitry shows EC, DG, CA1, CA3, and SB. A diagram shows a 'childhood' node with inputs from 'ice cream', 'family', 'high school', and 'home', and outputs to the same categories. A small image of a house is shown.

### Recurrent networks

- Extensive collateral connections in CA3
- Broader loop: EC -> CA3 -> CA1 -> EC

Figure 17: Recurrent networks. Includes a schematic of the hippocampal circuitry showing EC, DG, CA1, CA3, and SB.

$$\Delta w_{ij} = r_i r_j - r_i w_{ij}$$

Cells that fire together, wire together  
 Loop repeatedly increases weight – increasingly encourage simultaneous firing

Figure 17: Recurrent networks. Includes a schematic of the hippocampal circuitry showing EC, DG, CA1, CA3, and SB.

### Learning locations

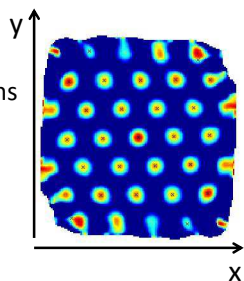
- Rats learn neural representations of locations within a maze
- Hippocampal place cells in CA1, CA3

Figure 18: Learning locations. A 3D plot shows a peak in a 2D space. Three diagrams (A, B, C) show connections between hippocampal neurons. A is 'Fully connected', B is 'After learning', and C is 'After learning (revisited)'. A caption below reads: 'Samsonowich, J Neurosci 1997 Neurons organized in 2D based on similarity of tuning curves'.

### Further hippocampal representations

#### Grid cells

- In dorsocaudal medial EC
- Represent multiple locations



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### Learning/remembering

- Learning: neurogenesis in DG
- Retrieval: pattern completion in CA3
- Alternate between learning and retrieval phases
  - DG granule cells enable learning
  - Perforant pathway probes memory

*Potential model*

### Modeling limits of working memory

- How much can we hold in working memory?
  - $7 \pm 2$  things
  - Things can be simple A Q R L G
  - Things can be complex

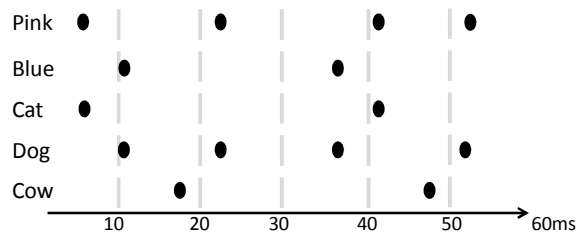


- Why is our working memory limited?
  - Binding hypothesis: distributed code with synchronous spiking – errors with spurious synchronization

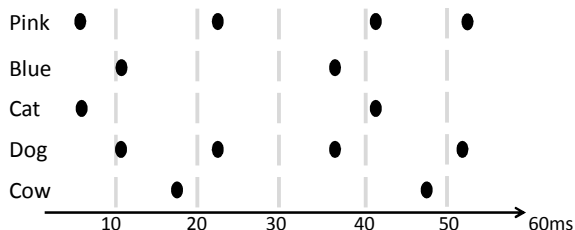
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### Binding problem

Neurons firing at “same time” represent same thing



### Spurious synchronization



If spikes occurring within 1 ms of each other are considered synchronous, hard to incorporate increasing number of spikes in fixed time

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